

Form PTO-1449 (Modified)				Atty. Docket No. 1662-23400 (PD99-2862)		Serial No. 09/723,687	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use several sheets if necessary)				Applicants Harish G. PATIL et al.			
				Filing Date November 28, 2000		Group 2183	

AUG 22 2001  
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REFERENCE DESIGNATION U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE

RECEIVED  
 AUG 27 2001  
 Technology Center 2100

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	Translation YES NO	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
	AA	Combining Static and Dynamic Branch Prediction to Reduce Destructive Aliasing; Harish Patil et al. (12 p.).
	AB	PhD Dissertation Static Methods in Branch Prediction; Donald Lindsay (2 p.).
	AC	Intel® IA-64 Architecture Software Developer's Manual; Volume 1: IA-64 Application Architecture; Revision 1.1; July 2000 (4p.).

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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**Translation**

**YES      NO**

Intel® Itanium™ Architecture Software Developer's Manual, *Volume 1: Application Architecture*, Revision 2.0, December 2001 (234 p.).

9/25/2003

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